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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Angelo Magri', Ferruccio Frisina
Title of Invention: Insulated Gate Planar Integrated Power Device With Co-Integrated Schottky Diode and Process
Serial No.: 10/713,778
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Attorney Dkt. No.: 02-CT-372/DP (2110-86-3)

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Signature

AMENDMENT AND RESPONSE UNDER 37 CFR §1.111

TO THE COMMISSIONER FOR PATENTS

Receipt of an Office Action mailed on October 28, 2005 in the above-referenced patent application is acknowledged. Please amend the application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 13 of this paper.